

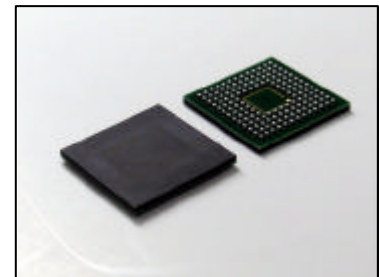
SSCD-J2, Diversity Receiver LSI for ISDB-T Broadcasting

1. Overview

- ▶ The SSCD-J2 is the 2 and 4 Antennas Diversity Receiver LSI for ISDB-T Broadcasting.
- ▶ Achieved high performance and the low power consumption and compliant with ISDB-T (ARIB-STD-B31 & ARIB-TR-B14), and supports both 12-segment and 1-segment.
- ▶ For Japan and South America DTV and Built-in EWS (Emergency Warning System)

2. Main Specifications

- Supply Voltage : 1.2±0.1V (Inside), 3.3±0.3V (I/O, BGR)
- Operating Temp. : -40C to +85C
- Package : 144-pin FBGA (12x12mm, 0.8mm ball pitch)
- Power : Typ. 270 to 310mW (2AD to 4AD active)
- IF Center Freq. : Low-IF & IQ Base-band
- In/output Format : IF differential Input (1.0Vpp), MPEG2 TSP
- Master Clock : 33MHz (±50ppm) connectable a crystal
- Channel bandwidth : 6MHz (Japan & South America)

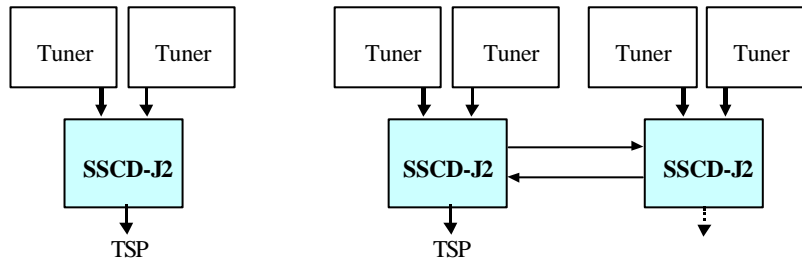


<PKG Photo>

3. System Construction

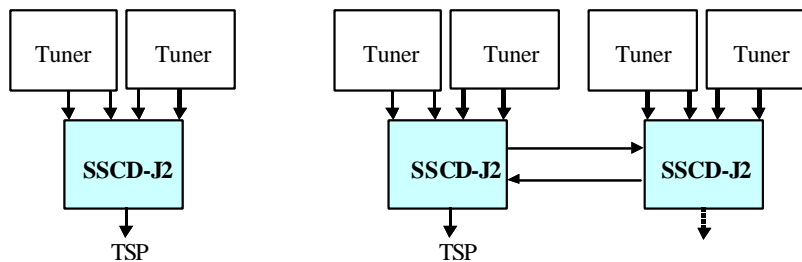
- ▶ Low-IF Input Mode * typ270mW

One realizes 2-antenna and two realizes 4-antenna diversity. 1-TSP or 2-TSP output is selectable.



- ▶ IQ Base-band Input Mode * typ.310mW

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- ▶ Smart Mode (4 Input, Low-IF) * typ.310mW

